



LENDI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Approved by AICTE & Permanently Affiliated to JNTUK, Kakinada

Accredited by NAAC with "A" Grade, NBA (CSE, ECE, EEE, ME)

Jonnada (Village), Denkada (Mandal), Vizianagaram Dist – 535 005

Phone No. 08922-241111, 241112

E-Mail: lendi_2008@yahoo.com

Website: www.lendi.org

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING


List of Projects (Academic Year: 2016-17)

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
1	Implementation of Barrel Shifter Using Diode Free Logic	VLSI	Research	PO1-PO12, PSO1, PSO2
2	Multilevel Fuzzy Threshold Image Segmentation Method for Industrial Application	Image Processing	Research	PO1-PO12, PSO1, PSO2
3	Performance Analysis of Different Transform Image Encoding and Decoding Techniques	Image Processing	Research	PO1-PO12, PSO1, PSO2
4	Dual Band/Tri Band/Broad Band CPW Fed Stepped Impedance Slot Dipole Antennas	Antennas	Research	PO1-PO12, PSO1, PSO2
5	Design of low voltage high performance (speed)full adder using FINFET	VLSI	Research	PO1-PO12, PSO1, PSO2
6	Quality assessment of Contrast distorted image	Image Processing	Research	PO1-PO12, PSO1, PSO2
7	Design of Automatic Accident Intimation Device Through SMS (GSM)	IoT & Embedded Systems	Application	PO1-PO12, PSO1, PSO2
8	High Performance Full Subtractor	VLSI	Research	PO1-PO12, PSO1, PSO2
9	Drowsiness Detection System	IoT & Embedded Systems	Application	PO1-PO12, PSO1, PSO2
10	DESIGN of microstrip patch antenna	Antennas	Research	PO1-PO12, PSO1, PSO2
11	Multi slot Microstrip Antenna Using UWB Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
12	A High-speed low power Parity BIT CAM with Power Gated ML Sensing	VLSI	Research	PO1-PO12, PSO1, PSO2

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
13	ANN Based Detector to Remove Random Valued Impulse Noise in Images	Image Processing	Research	PO1-PO12, PSO1, PSO2
14	Design and Investigation of Graphene Based Tera-Hertz Antenna	Antennas	Research	PO1-PO12, PSO1, PSO2
15	Design of Microstrip Patch Antenna for Low RCS Used in Stealth Technology	Antennas	Research	PO1-PO12, PSO1, PSO2
16	Automatic Vehicle Number Detection using MATLAB	Image Processing	Research	PO1-PO12, PSO1, PSO2
17	New Multifunctional Di-Gates & Its Applications in Code Converters	VLSI	Research	PO1-PO12, PSO1, PSO2
18	Modulatory Based Image Segmentation	Image Processing	Research	PO1-PO12, PSO1, PSO2
19	Implementation of Square Root Carry Select Adder using Zero Finding Logic	VLSI	Research	PO1-PO12, PSO1, PSO2
20	Smart Home System using Android Mobile	IoT & Embedded Systems	Application	PO1-PO12, PSO1, PSO2
21	Data Retrieval and Image Retrieval Method Using SRC	Image Processing	Research	PO1-PO12, PSO1, PSO2
22	Design of Low Power FINFET Adiabatic Circuits	VLSI	Research	PO1-PO12, PSO1, PSO2
23	3-D Liver Image Segmentation	Image Processing	Research	PO1-PO12, PSO1, PSO2
24	Face Recognition using Neural Networks	Image Processing	Research	PO1-PO12, PSO1, PSO2
25	Design of Energy Efficient Multiplier using GDI Logic	VLSI	Research	PO1-PO12, PSO1, PSO2
26	Adaptive Routing of Traffic Signaling using Image Processing	Image Processing	Research	PO1-PO12, PSO1, PSO2
27	Low Voltage High-Performance Hybrid Full-Adder	VLSI	Research	PO1-PO12, PSO1, PSO2
28	Identifying Multiple Plant Diseases using Image Processing	Image Processing	Research	PO1-PO12, PSO1, PSO2
29	Text Independent Voice Based Student Attendance System by RASTA-MFCC	Signal Processing	Research	PO1-PO12, PSO1, PSO2

S.No	Project Title	Domain	Classification	Relevant to POs & PSOs
30	Ultra-Wideband [UWB] Printed Monopole Antenna for WLAN & Satellite Communication	Antennas	Research	PO1-PO12, PSO1, PSO2
31	Circularly Polarized Hybrid Z-shaped Cylindrical Dielectric Resonator Antenna for Multiband Applications	Antennas	Research	PO1-PO12, PSO1, PSO2
32	Design and Implementation of AMBA, AHB & APB Bridge Based on Memory Controller	VLSI	Research	PO1-PO12, PSO1, PSO2
33	Image Compression Using SPIHT Algorithm	Image Processing	Research	PO1-PO12, PSO1, PSO2
34	Performance Analysis and Comparison of ITO & FTO Based Transparent U-Shaped Patch Antennas	Antennas	Research	PO1-PO12, PSO1, PSO2
35	High Performance and Low Power ONOFIC Approach for VLSI CMOS Circuit Design	VLSI	Research	PO1-PO12, PSO1, PSO2
36	Filtering Techniques for Denoising of QR Code	Image Processing	Research	PO1-PO12, PSO1, PSO2
37	Design of High-speed VEDIC Multiplier	VLSI	Research	PO1-PO12, PSO1, PSO2
38	A Novel Low Power Full Adder Using Hybrid CMOS Logic	VLSI	Research	PO1-PO12, PSO1, PSO2
39	Analysis of Low-Voltage, Low-Power Double-Tail Comparator	VLSI	Research	PO1-PO12, PSO1, PSO2
40	A Low-Power Reconfigurable Encoder for Flash ADCs	VLSI	Research	PO1-PO12, PSO1, PSO2
41	BER Analysis of MIMO-OFDM System Using Various Modulation Techniques	Communications	Research	PO1-PO12, PSO1, PSO2
42	Geometric Parameters of Multi-core Optical Fibers	Communications	Research	PO1-PO12, PSO1, PSO2


Project Coordinator


Head of the Department, ECE
Head of the Department,
Electronic & Communication Engineering
JNTU Institute of Engg. & Technology
Kamada- 506003, Visakhapatnam Dist.